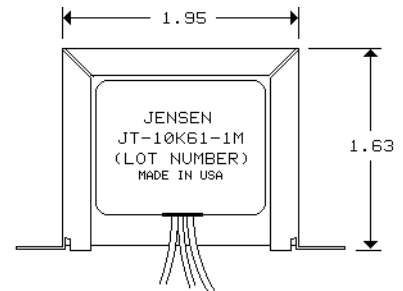


Line Output Transformer

4:1 CT or 8:1 with FARADAY SHIELD

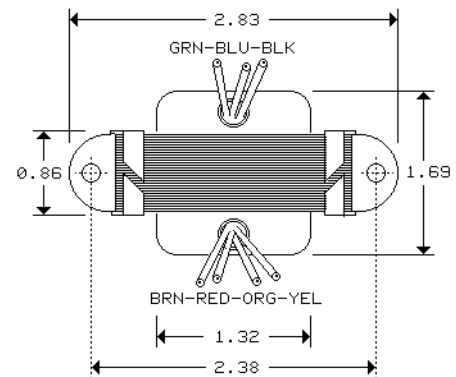
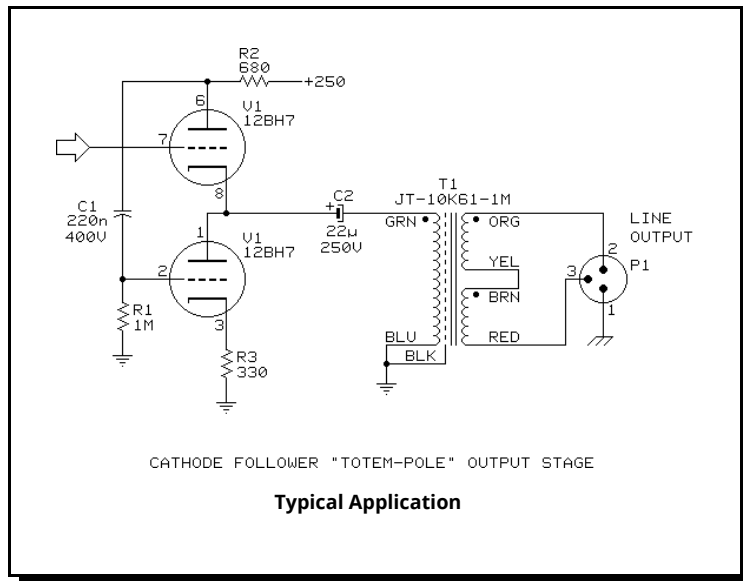
- Distortion 0.007% typ at 20 Hz and +4 dBu output level
- Wide bandwidth: -3 dB at 0.04 Hz and 60 kHz
- Drives 600 Ω loads to levels up to +23 dBu at 20 Hz
- Excellent time domain performance: DLP 0.2° typ 20 Hz to 20kHz
- Appears as 11 k Ω load to vacuum tube driver circuitry

This transformer is designed for very high performance vacuum tube line output stages. Driving signals should be free of DC and source impedance under 1 k Ω . The split secondaries may be series connected for 4:1 with center-tap, or paralleled for 8:1 operation. A fully enclosed channel frame is standard.

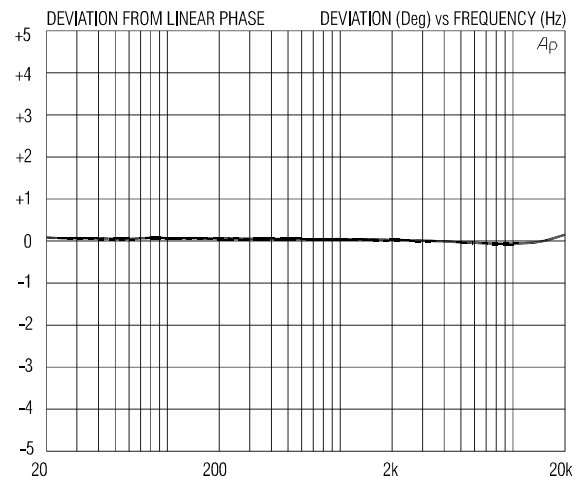
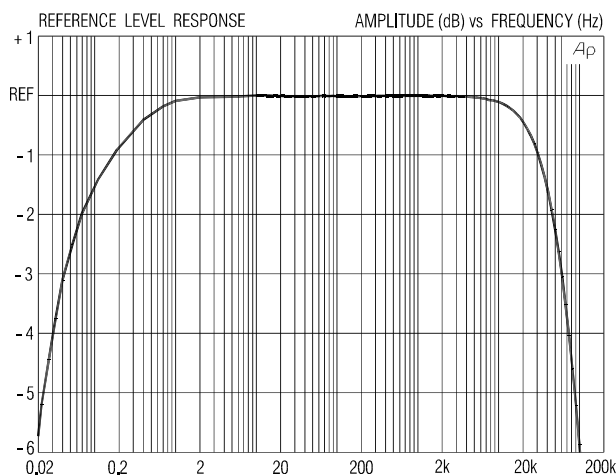


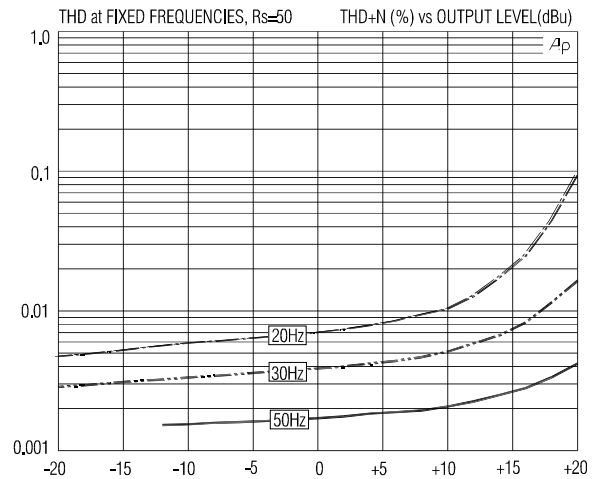
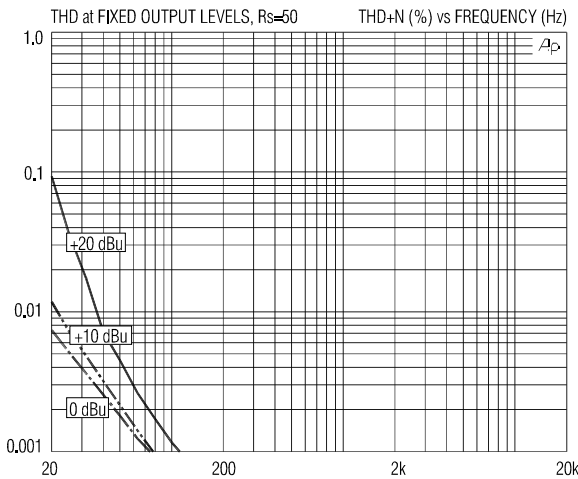
#26 AWG (7x34) UL STYLE 1061 COLOR CODED WIRE LEADS, 8" MINIMUM LENGTH

LEAD HOLES SPACED 1.20 CENTER TO CENTER



0.187 DIA MOUNTING HOLES (2 PLACES)
FOR USE WITH #8 MOUNTING HARDWARE

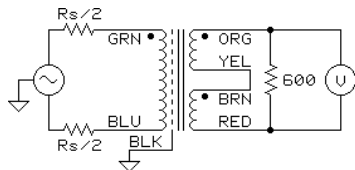




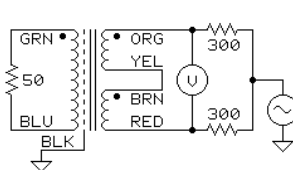
JT-10K61-1M SPECIFICATIONS (4:1 series secondaries configuration, all levels are output unless noted)

PARAMETER	CONDITIONS	MINIMUM	TYPICAL	MAXIMUM
Input impedance, Z_i	1 kHz, 0 dBu, test circuit 3	10.0 k Ω	11.5 k Ω	13.0 k Ω
Voltage gain	1 kHz, 0 dBu, test circuit 1	-13.8 dB	-13.4 dB	-13.0 dB
Magnitude response, ref 1 kHz	20 Hz, 0 dBu, test circuit 1, $R_s=50 \Omega$	-0.1 dB	-0.01 dB	0.0 dB
	20 kHz, 0 dBu, test circuit 1, $R_s=50 \Omega$	-0.5 dB	-0.37 dB	0.0 dB
Deviation from linear phase (DLP)	20 Hz to 20 kHz, 0 dBu, test circuit 1, $R_s=50 \Omega$		+0.2/-0.1°	$\pm 2.0^\circ$
Distortion (THD)	1 kHz, +4 dBu, test circuit 1, $R_s=50 \Omega$		<0.001%	
	20 Hz, +4 dBu, test circuit 1, $R_s=50 \Omega$		0.008%	0.05%
	1 kHz, +4 dBu, test circuit 1, $R_s=600 \Omega$		<0.001%	
	20 Hz, +4 dBu, test circuit 1, $R_s=600 \Omega$		0.020%	
Maximum output level	20 Hz, 1% THD, test circuit 1, $R_s=50 \Omega$	+21 dBu	+23 dBu	
Common-mode rejection ratio (CMRR)	60 Hz, test circuit 2		114 dB	
	3 kHz, test circuit 2	70 dB	80 dB	
Output impedance, Z_o	1 kHz, test circuit 4, $R_s=50 \Omega$		133 Ω	
DC resistance	primary (GRN to BLU)		740 Ω	
	secondaries in series (ORG to RED)		42 Ω	
Capacitance	primary to shield and case, 1 kHz		300 pF	
	both secondaries to shield and case, 1 kHz		300 pF	
Turns ratio	secondaries in series	4.070:1	4.082:1	4.095:1
Temperature range	operation or storage	0° C		70° C
Breakdown voltage (see IMPORTANT NOTE below)	primary or secondary to shield and case, 60 Hz, 1 minute test duration	250 V RMS		

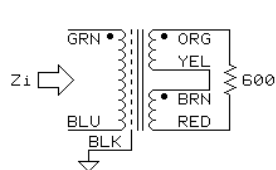
IMPORTANT NOTE: This device is NOT intended for use in life support systems or any application where its failure could cause injury or death. The breakdown voltage specification is intended to insure integrity of internal insulation systems; continuous operation at these voltages is NOT recommended. Consult our applications engineering department if you have special requirements.



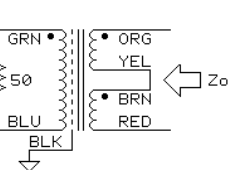
TEST CIRCUIT 1



TEST CIRCUIT 2



TEST CIRCUIT 3



TEST CIRCUIT 4

All minimum and maximum specifications are guaranteed. Unless noted otherwise, all specifications apply at 25°C. Specifications subject to change without notice. All information herein is believed to be accurate and reliable, however no responsibility is assumed for its use nor for any infringements of patents which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Jensen Transformers, Inc.